

Electronic Acknowledgement Receipt

EFS ID:	4828031		
Application Number:	10786838		
International Application Number:			
Confirmation Number:	2958		
Title of Invention:	Digital signal processor having data address generator with speculative register file		
First Named Inventor/Applicant Name:	James F Galeotos		
Correspondence Address:	William R. McClellan Wolf, Greenfield & Sacks, P.C. 600 Atlantic Avenue - Boston MA 02210 US 6177203500 wmcclellan@wolfgreenfield.com		
Filer:	William R. McClellan/Patricia Marchetti		
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Application Type:	Utility under 35 USC 111(a)		

Payment information:

Submitted with Payment	no
File Listing:	

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	A031270525US00-TRN-WRM.PDF	30746 1b6f97600e856436ef96e896550478f5d9913	no	1
Warnings:					
Information:					
2		A031270525US00-116AMN-WRM.PDF	409768 1a+937c1b99ca11802017a5fffc94ec223810f79	yes	12
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Amendment After Final			1	1	
Claims			2	5	
Applicant Arguments/Remarks Made in an Amendment			6	12	
Warnings:					
Information:					
Total Files Size (in bytes):				440514	
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